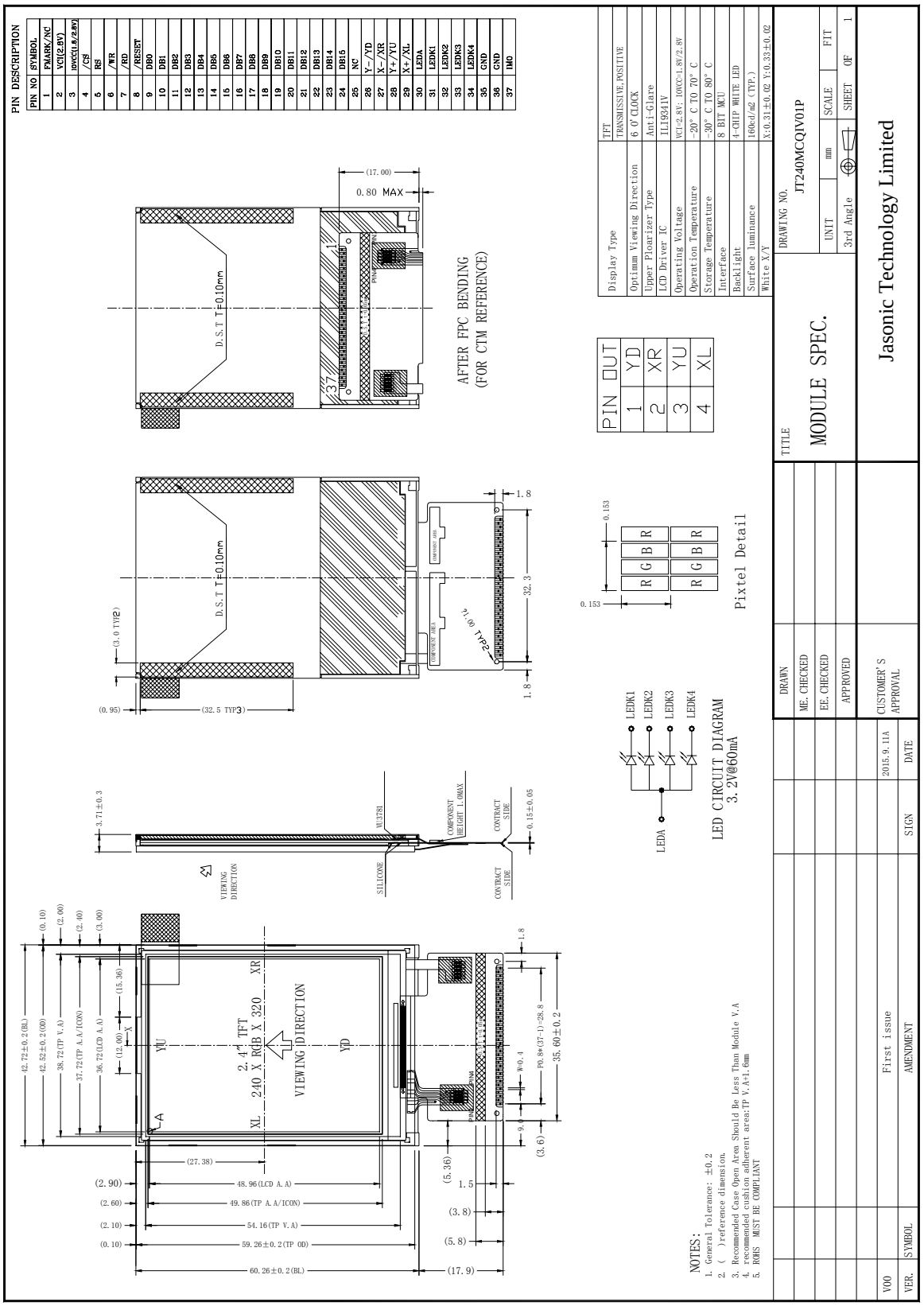
PIN DESCRIPTION
PIN NO SYMBOL
1 FMARK/NC
2 VCI(2.5V)
3 IOVCC(1.5/2.8V)
4 /CS
5 RS
6 /WR
7 /RD
8 /RESET
9 DB0
10 DB1
11 DB2
12 DB3
13 DB4
14 DB5
15 DB6
16 DB7
17 DB8
18 DB9
19 DB10
20 DB11
21 DB12
22 DB13
23 DB14
24 DB15
25 NC
26 Y-/YD
27 X-/XR
28 Y+/YU
29 X+/XL
30 LEDA
31 LEDK1
32 LEDK2
33 LEDK3
34 LEDK4
35 GND
36 GND
37 IMO
Display Type TFT
Optimum Viewing Direction TRANSMISSIVE,POSITIVE
Upper Ploarizer Type 6 O'CLOCK
LCD Driver IC Anti-Glare
Operating Voltage ILI9341V
Operation Temperature VCI=2.8V: IOVCC=1.8V/2.8V
Storage Temperature -20° C TO 70° C
Interface -30° C TO 80° C
Backlight 8 BIT MCU
Surface luminance 4-CHIP WHITE LED
White X/Y 160cd/m2 (TYP.)
X:0.31±0.02 Y:0.33±0.02
AFTER FPC BENDING
(FOR CTM REFERENCE)
D.S.T T=0.10mm
(17.00)
0.80 MAX
PIN OUT
1 YD
2 XR
3 YU
4 XL
Pixtel Detail
R G B R
LED CIRCUIT DIAGRAM
3.2V@60mA
LEDK1
LEDK2
LEDK3
LEDK4
LEDA
VIEWING DIRECTION
2.4" TFT
240 X RGB X 320
XL
XR
YU
YD
SILICONE
COMPONENT HEIGHT 1.0MAX
CONTRACT SIDE
NOTES:
1. General Tolerance: ±0.2
2. ( ) reference dimension.
3. Recommended Case Open Area Should Be Less Than Module V.A
4. recommended cushion adherent area:TP V.A+1.6mm
5. ROHS MUST BE COMPLIANT
TITLE
MODULE SPEC.
DRAWING NO.
JT240MCQIV01P
UNIT mm
SCALE FIT
3rd Angle
SHEET OF 1
Jasonic Technology Limited
DRAWN
ME. CHECKED
EE. CHECKED
APPROVED
CUSTOMER'S APPROVAL
V00 First issue 2015.9.11A
VER. SYMBOL AMENDMENT SIGN DATE